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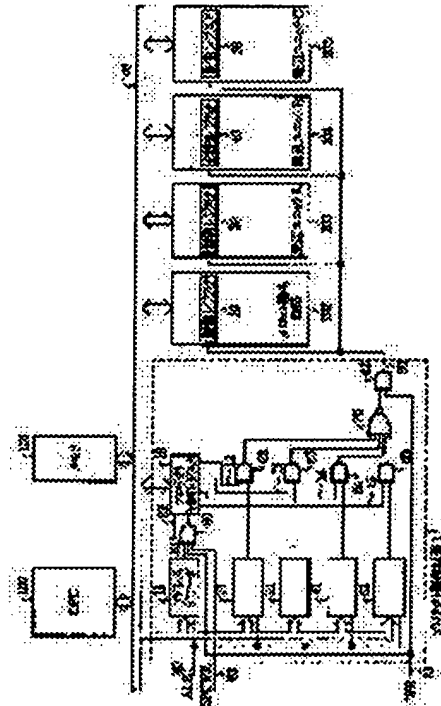
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(57)Abstract:

PURPOSE: To completely prevent the wrong writing to the control registers without using any exclusive instruction by deciding whether a write signal should be granted or inhibited in response to the information on a protection control register corresponding to the specified control register.

CONSTITUTION: A protection control register 10 holds the information for each of control registers 20 to 50 to decide whether the data writing operations should be granted or inhibited to the registers 20 to 50 which are contained in the peripheral circuits 202 to 205 respectively. The address decoders 21 to 51 specify the writing object registers among the registers 20 to 50 when a writing operation is carried out. Then it is decided whether the write signal should be granted or inhibited in response to the information on the register 10 corresponding to the control registers that are specified by the decoders 21 to 51. When the writing operations are carried out to the registers 20 to 50, the write instruction signal 90 is controlled against the circuits 202 to 205 based on the address information and the output of the register 10.



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